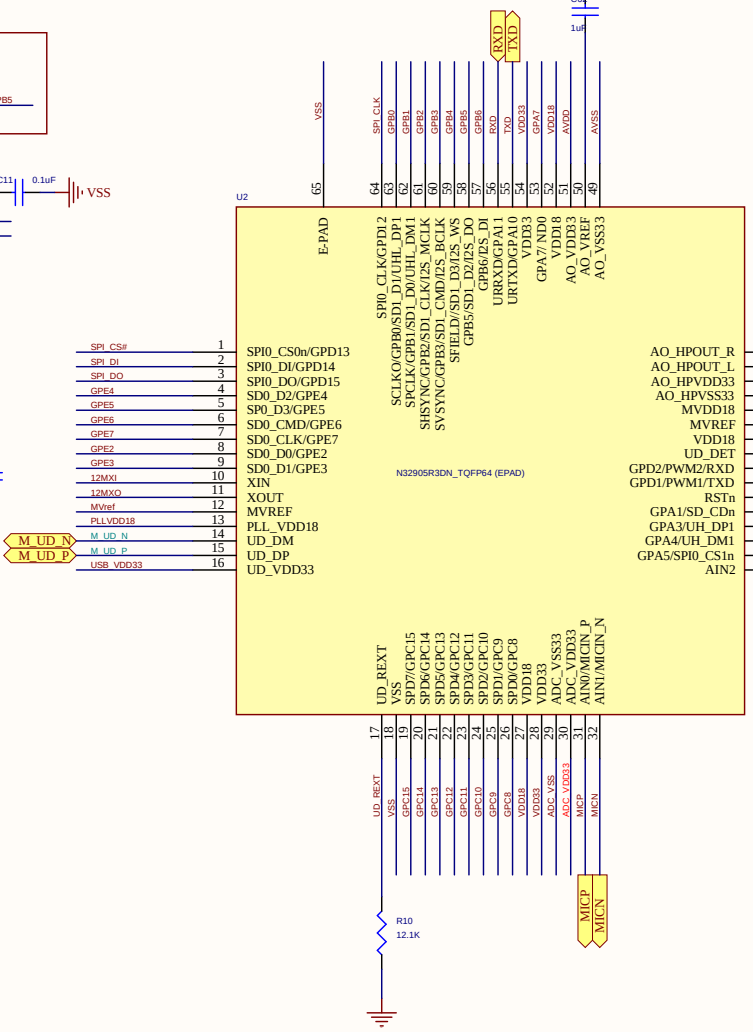
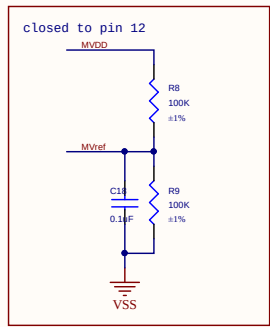
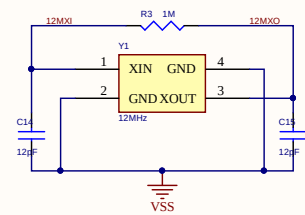
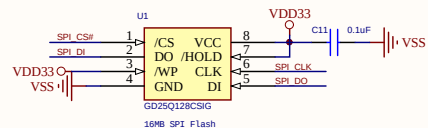
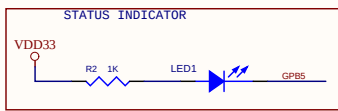
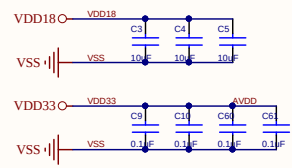
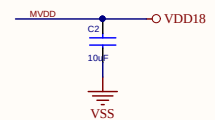
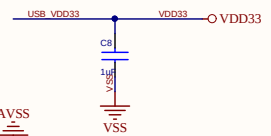
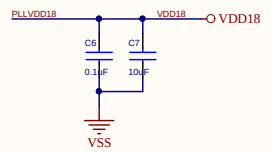
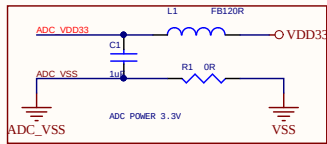
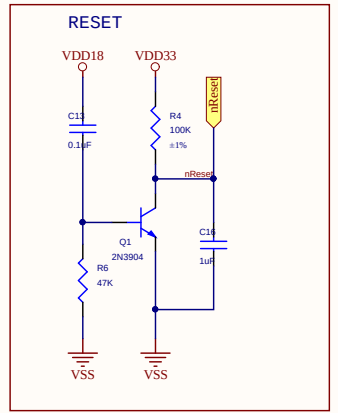
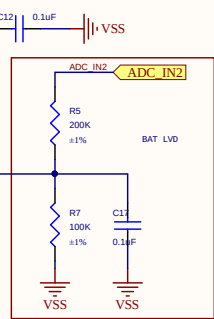
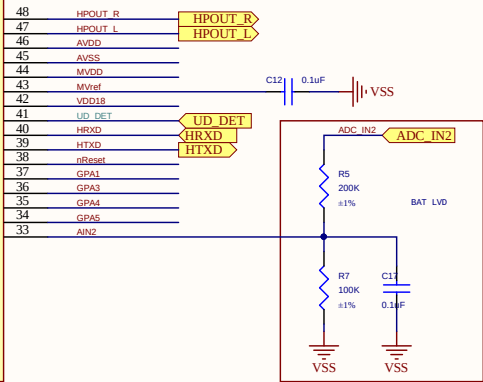


MARK1
MARK2
MARK3



GPA1[1:11] → GPA1[1..11]
GPB0[6] → GPB0[0..6]
GPC8[8..15] → GPC8[8..15]
GPE2[2..7] → GPE2[2..7]



1

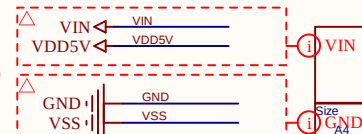
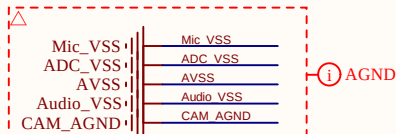
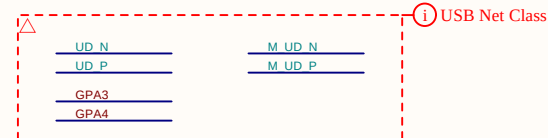
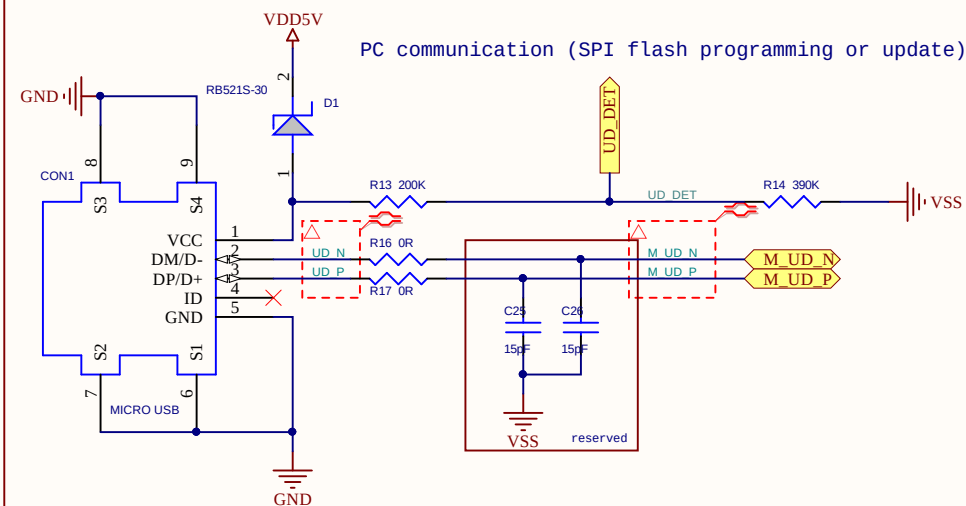
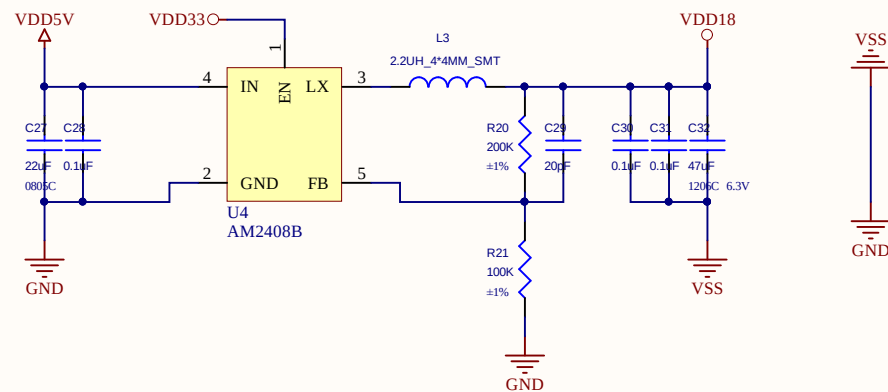
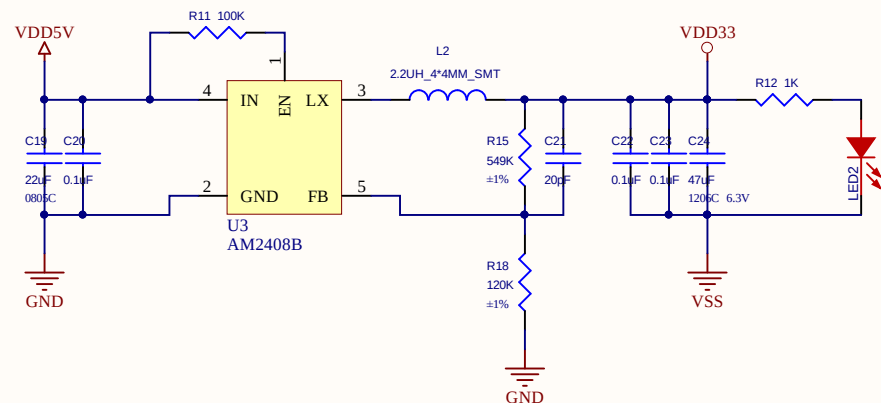
2

3

4

Vin=3.4V--5.5V

Power Supply with DC-DC

 $V_{out}=0.6*(1+R1/R2)$ 

VIN			
RAK_Arduino_VGACAM			
Size	Document Number		Rev
A4	Power		1.0
Date:	Sheet		2 of 4

1

2

3

4



The RF trace and the copper ground (via, per 1.5mm) is 1.5X the trace width
please layout 6 mil core and 12 mil trace width
If the RF trace keep 50 ohm characteristic impedance

